

DIGITAL ELECTRONICS

1

Week 1

Analogue / Digital Signals, Digital Levels
Binary Code, Binary <-> Decimal
Basic Gates, AND, OR, NOT (*Symbols, Boolean, Truth Table*)

9 week program
@
8 hours per week

2

Week 2

XOR, NAND, NOR, XNOR (*Symbols, Boolean, Truth Table*)
NAND, NOR as Universal, Combinational Logic

Draft Delivery
@
April 2018

3

Week 3

Boolean, De Morgans

Week 4

K Maps

Week 5

Number Systems

* see new assessment guidelines

Week 6

ESD
EKAS Assessment 1 (*Covering all Work*) *

4

Week 7

Displays

Week 8

SR, D, T Flip Flops

5

Week 9

JK Flip Flops, Shift Registers

Wk 10

Async Counters

6

Wk 11

Sync Counters

Wk 12

Monostables, Astables

7

Wk 13

Fault Finding
EKAS Assessment 2 (*Covering all Work*) *

Wk 14

Encoder/Decoder, Mux/Demux

8

Wk 15

Logic Families, Interfacing

Wk 16

A/D, D/A, Prog Logic

9

Wk 17

Project Assignment Due (Project A/D & D/A) *
Revision, Practical Test

Wk 18

Final Theory (*Covering all Work*) *